

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	09/751748	Docket Number	MIPS.0107-00-US
Filed	12/29/2000	Group Art Unit	2181
Examiner	WILLIAM M. TREAT	Customer No.	23669
Application Title	CONFIGURABLE CO-PROCESSOR INTERFACE		
First Named Inventor	LAWRENCE H. HUDEPOHL		

INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

Mail Stop **AMENDMENT**
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted,
HUFFMAN LAW GROUP, P.C.

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07/17/06

Date: _____

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
				Application Number	09/751748
				Filing Date	12/29/2000
				First Named Inventor	Lawrence H. Hudepohl
				Group Art Unit	2181
				Examiner Name	William M. Treat
Sheet	1	of	1	Attorney Docket Number	MIPS.0107-00-US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		EGGERS, SUSAN J. et al. "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor." University of Washington. 1990 <i>ACM</i> , pp. 37-47.	
		UHLIG, RICHARD A. et al. "Trace-Driven Memory Simulation: A Survey." Intel Microcomputer Research Lab; University of Michigan, <i>ACM Computing Surveys</i> . Vol. 29, No. 2, June 1997. pp. 128-170.	
		ROTENBERG, ERIC et al. "Trace Processors." University of Wisconsin. 1997 <i>IEEE Service Center</i> . 12 pp.	
		ELNOZAHY, E.N. "Address Trace Compression Through Loop Detection and Reduction." 1999 <i>ACM</i> 1-58113-083, pp. 214, 215.	
		JONES, DARREN. MIPS64™ 5Kc™ Processor Cores User's Manual. Rev. 1.0, July 4, 1999. pp. 6-1 to 6-26.	
		CHOW, F. et al. "Engineering a RISC Compiler System." <i>IEEE COMP-CON</i> . March 1986. pp. 132-137.	
		SMITH, M.D. "Tracing with Pixie." <i>Technical Report</i> CSL-TR-91-497. Stanford University, Computer Systems Laboratory, November 1991. pp.1-29	
		ATOM Reference Manual. <i>Digital Equipment Corporation</i> . Massachusetts, December 1993. pp.1-32	
		SRIVASTAVA, A. et al. "ATOM: A System for Building Customized Program Analysis Tools." <i>WRL, Research Report</i> 94/2, Digital Equipment Corporation. March 1994, pp.1-23	
		ATOM User Manual, Digital Equipment Corporation, March 1994. pp. 1-28	
		CMELIK, B. et al. "Shade: A Fast Instruction-Set Simulator for Execution Profiling." <i>Proceedings of the 1994 ACM SIGMETRICS Conference</i> . SIGMETRICS, California. May 1994. pp. 128-137.	
		MIPS64 5Kc™ Processor Core Datasheet. Revision 1.7.5. August 11, 2000. Document Number MD00014	
		MIPS64 5Kc™ Processor Core Datasheet. Revision 2.0. August 28, 2000. Document Number MD00014	
		MIPS64™ 5Kf™ Processor Core Datasheet. Revision 00.11. March 30, 2001. Document Number: MD00109.	
		MIPS64 5K™ Processor Core Family Integrator's Guide. Revision 02.00 January 15, 2001. Document Number: MD00106.	
		MIPS64 5Kc™ Processor Core Integrator's Guide. Revision 1.2 August 11, 2000.. Document Number: MD00056.	
		MIPS Technologies. "Core Coprocessor Interface Specification." Revision 1.11 March 30, 2001 Document Number: MD00068	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**

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